# In the Specification:

On page 1 of the application after the title, please insert the following:

## -- CROSS-REFERENCE TO RELATED APPLICATION

The following application claims priority to Application No. PCT/DE00/01735, filed on May 29, 2000.

# **BACKGROUND**

#### Field of the Invention-

Please replace the paragraph beginning on page 1, line 4, with the following rewritten paragraph:

--The invention relates, generally, to a circuit arrangement for generating clock pulses in a communications system, more particularly, to a circuit arrangement for a communication system for terminating a plurality of interfaces at a common bus and for generating a synchronization clock for synchronizing the bus as claimed in the preamble of claim 1.--

On page 1, line 7, please insert the following heading:

## -- Discussion of Related Art--

On page 5, line 10, please insert the following heading:

--SUMMARY OF THE INVENTION—



Please replace the paragraph beginning on page 5, line 20, with the following rewritten paragraph:

--This object is achieved by a circuit arrangement having the features of claim 1 at least one network termination, where each network termination can be connected to at least one transmission line in each case and to a bus and where a clock is provided for synchronizing the bus characterized in that a multiplicity of clock generators for generating the clock and means for selecting a clock generator are provided. Preferred embodiments of the invention are the subject matter of the dependent claims.--

On page 7, line 20, please insert the following heading:

# --BRIEF DESCRIPTION OF THE DRAWINGS--

On page 8, line 10, please insert the following heading:

## -- DETAILED DESCRIPTION OF EXEMPLARY EMBODIMENTS--

Please replace the heading on page 12, line 1, with the following rewritten heading:
--Patent Claims What is claimed is:--